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1 of 8 Data Selectors/Multiplexers (with 3-state outputs)



ADE-205-476 (Z) 1st. Edition Sep. 2000

Description

This multiplexer features both true (Y) and complement (W) outputs as well as a strobe input. The strobe must be at a low logic level to enable this device. When the strobe input is high, both outputs are in the high impedance state. When enabled, address information on the data select inputs determines which data input is routed to the Y and W outputs.

Features

• High Speed Operation: t_{pd} (A, B, C to Y) = 20 ns typ ($C_L = 50 \text{ pF}$)

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$

• Low Input Current: 1 μA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

Function Table

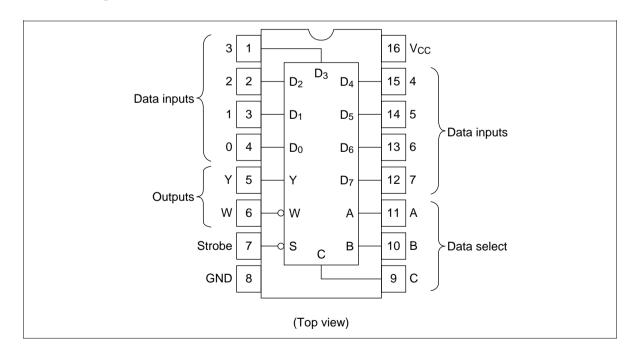
Inputs

Select			Strobe	Outputs	
С	В	Α	S	Y	W
X	Х	Х	Н	Z	Z
L	L	L	L	D _o	$\overline{D}_{\!\scriptscriptstyle{0}}$
L	L	Н	L	D ₁	\overline{D}_{1}
L	Н	L	L	$D_{\scriptscriptstyle 2}$	$\overline{D}_{\!\scriptscriptstyle 2}$
L	Н	Н	L	D ₃	$\overline{D}_{\!\scriptscriptstyle 3}$
Н	L	L	L	D_4	$\overline{D}_{\!\scriptscriptstyle{4}}$
Н	L	Н	L	$D_{\scriptscriptstyle{5}}$	$\overline{D}_{\scriptscriptstyle{5}}$
Н	Н	L	L	D_6	$\overline{D}_{\!\scriptscriptstyle{6}}$
Н	Н	Н	L	D ₇	\overline{D}_{7}

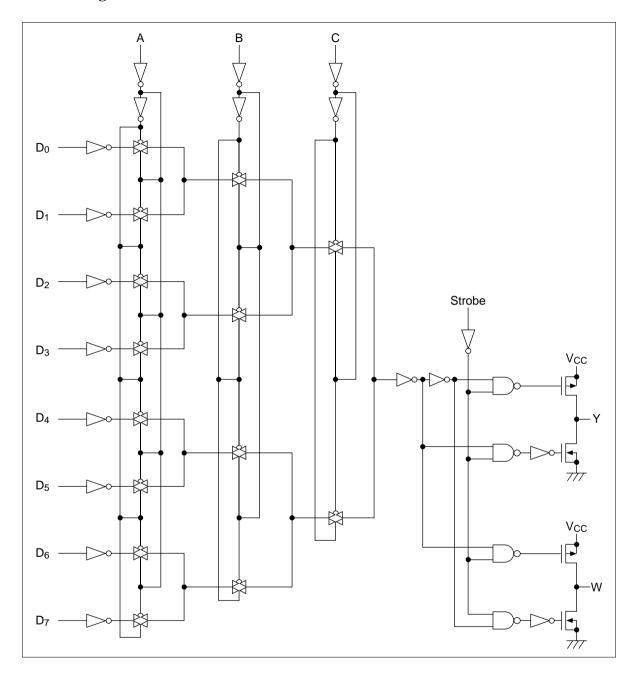
Notes 1. H: high level, L: low level, X: irrelevant

- 2. Z; high impedcance (off-state)
- 3. D_0 through D_7 ; the level of the respective D input.

Pin Arrangement



Block Diagram



DC Characteristics

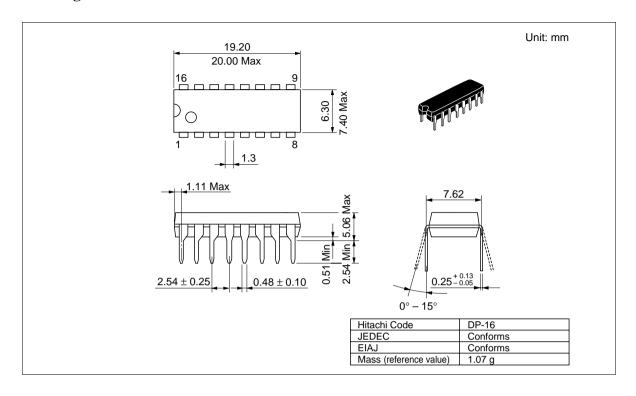
			Ta = 25°C		Ta = -40 to +85°C					
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	าร
Input voltage	V_{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_	_		
		6.0	4.2	_	_	4.2	_	_		
	V _{IL}	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35	-		
		6.0	_	_	1.8	_	1.8	_		
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_	-		
		6.0	5.9	6.0	_	5.9	_	=		
		4.5	4.18	_	_	4.13	_	_		$I_{OH} = -4 \text{ mA}$
		6.0	5.68	_	_	5.63	_	_		$I_{OH} = -5.2 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	_	0.0	0.1	_	0.1	_		
		6.0	_	0.0	0.1	_	0.1	_		
		4.5	_	_	0.26	_	0.33	_		I _{OL} = 4 mA
		6.0	_	_	0.26	_	0.33	_		I _{OL} = 5.2 mA
Off-state output current	I _{oz}	6.0	_	_	±0.5	_	±5.0	μΑ	$Vin = V_{IH} \text{ or } V_{IL},$ $Vout = V_{CC} \text{ or } G$	
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or GN	ND
Quiescent supply current	I _{cc}	6.0	_	_	4.0	_	40	μΑ	Vin = V _{CC} or GN	ND, lout = $0 \mu A$

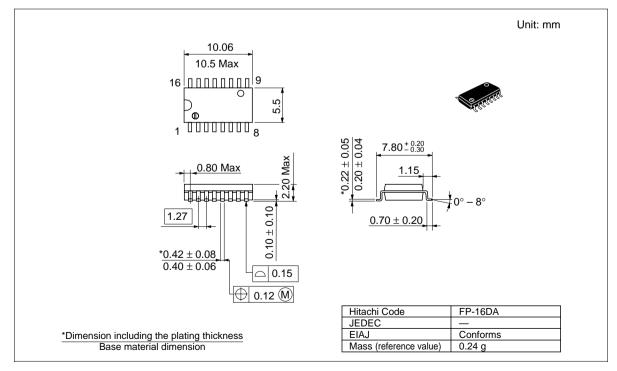
AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

Ta = -40 to $Ta = 25^{\circ}C$ +85°C

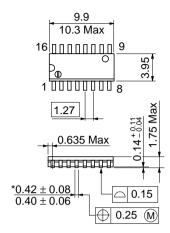
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	205	_	255	ns	A, B or C to Y
time	t _{PHL}	4.5	_	20	41	_	51	_	
		6.0	_	_	35	_	43	_	
	t _{PLH}	2.0	_	_	205	_	255	ns	A, B or C to W
	$t_{\tiny PHL}$	4.5	_	20	41	_	51	_	
		6.0	_	_	35	_	43	_	
	t _{PLH}	2.0	_	_	195	_	245	ns	Data to Y
	$t_{\tiny PHL}$	4.5	_	17	39	_	49	_	
		6.0	_	_	33	_	42	_	
	t _{PLH}	2.0	_	_	185	_	230	ns	Data to W
	$t_{\tiny PHL}$	4.5	_	17	37	_	46	_	
		6.0	_	_	31	_	39	_	
Output enable	t _{zH}	2.0	_	_	150	_	190	ns	strobe to W $R_L = 1 \text{ k}\Omega$
time	t_{zL}	4.5	_	11	30	_	38	_	
		6.0	_	_	26	_	33	_	
	t _{zH}	2.0	_	_	145	_	180	ns	strobe to Y $R_L = 1 \text{ k}\Omega$
	t_{zL}	4.5	_	11	29	_	36	_	
		6.0	_	_	25	_	31	_	
Output disable	t _{HZ}	2.0	_	_	220	_	275	ns	strobe to W $R_L = 1 \text{ k}\Omega$
time	t_{LZ}	4.5	_	12	44	_	55	_	
		6.0	_	_	37	_	47	_	
	t _{HZ}	2.0	_	_	195	_	245	ns	strobe to Y $R_L = 1 \text{ k}\Omega$
	t_{LZ}	4.5	_	12	39	_	49	_	
		6.0	_	_	33	_	42	_	
Output rise/fall	t _{TLH}	2.0	_	_	75	_	90	ns	
time	t_{THL}	4.5	_	5	15	_	19	_	
		6.0	_	_	13	_	16	_	
Input capacitance	Cin	_	_	5	10	_	10	pF	

Package Dimensions

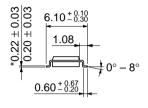




Unit: mm







*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DN			
JEDEC	Conforms			
EIAJ	Conforms			
Mass (reference value)	0.15 g			

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